

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	31893	(imprint\$6 or mold\$6 or template) and ((substrate or wafer or Si or silicon or semiconductor) with (orientat\$6 or lattice or crystal\$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 14:55
L2	119073	(imprint\$6 or mold\$6 or template) and ((substrate or wafer or Si or silicon or semiconductor) with (orientat\$6 or lattice or crystal\$6) with "[111]" or "(111)" or "<111>" or "[111]")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:01
L3	246963	(imprint\$6 or mold\$6 or template) and ((substrate or wafer or Si or silicon or semiconductor) with (orientat\$6 or lattice or crystal\$6) with "[110]" or "(110)" or "<110>" or "[110]")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:02
L4	72617	2 and 3 and (@ad<"20040416" or @rlad<"20040416")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:03
L5	42692	4 and (trench or groov\$6 or recess\$6 or opening)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:04
L6	5807	5 and ((trench or groov\$6 or recess\$6 or opening) same (substrate or Si or silicon or wafer or semiconductor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:06
L7	245	5 and ((trench or groov\$6 or recess\$6 or opening) same (substrate or Si or silicon or wafer or semiconductor) same (lattice or crystal\$6 or orientation) same ("110" or "<110>" or "(110)" or "[110]" or "[111]" or "<111>" or "(111)" or "[111]"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:09